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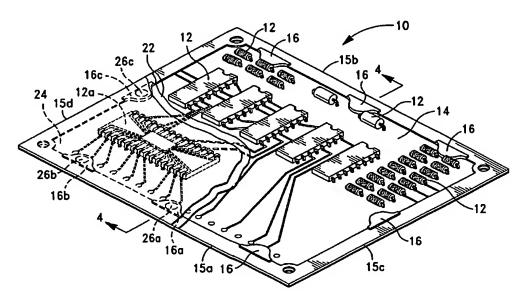
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[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR REDUCING ELECTROMAGNETIC EMISSIONS FROM ELECTRONIC CIRCUITS



(57) Abstract: An electronic circuit (10) comprising at least one electrical component (12) and at least one grounding point (16) is provided that includes a first layer of non-conductive coating (18) and a second layer of conductive coating (20). The non-conductive coating (18) is applied over the electrical component (12) in such a manner that the grounding point (16) remains uncoated. The conductive coating (20) is applied over the non-conductive coating (18) and the grounding point (16) so as to ground the conductive coating (20). The conductive coating (20) thus shields the electrical component (12) to thereby reduce electromagnetic emissions from the electronic circuit (10). Various exemplary embodiments of the coated electronic circuit and associated method are provided.





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IPC(7) : H05K 9/00; H05K 7/20, 9/00; H05K 1/00 US CL : 361/816, 818, 800; 257/655, 660; 174/35R, 35MS, 260		
US CL: 361/816, 818, 800; 257/655, 660; 174/35R, 35MS, 260 According to International Patent Classification (IPC) or to both national classification and IPC		
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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category * Citation of document, with indication, where	appropriate, of the relevant passages	Relevant to claim No.
	US 5,166,864 A (CHITWOOD et al.) 24 November 1992 (24.11.1992), see entire 1-11 document.	
Y US 5,557,064 A (ISERN-FLECHA et al.) 17 Sept	US 5,557,064 A (ISERN-FLECHA et al.) 17 September 1996 (17.09.1996), see entire document.	
	US 5,639,989 A (HIGGINS, III) 17 June 1997 (17.06.1997), see entire document.	
Further documents are listed in the continuation of Box C.	See patent family annex.	
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